

1 **IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

2 Application Serial No. ....09/665,920  
3 Filing Date ..... September 20, 2000  
4 Inventorship .....Haba et al.  
Applicant ..... Rambus Inc.  
5 Group Art Unit .....2841  
Examiner ..... Phan, T.  
6 Attorney's Docket No. ....RB1-008US  
7 Title: Multi-Channel Memory Architecture

8 To: Honorable Commissioner for Patents  
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13 **RESPONSE TO FINAL OFFICE ACTION OF APRIL 20, 2004**

14  
15 Sir:

16 In response to the Office action of April 20, 2004, the following is  
17 presented:

18 Listing of claims begins on page 2 of this paper.

19 Remarks/Arguments begin on page 7 of this paper.

20 Claims 2-5, 6, 8, 11-15, 19, 25, 26, 28-30, and 35-38 have been amended.  
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1. Fee Transmittal
2. Response to Final Office Action Dated April 20, 2004

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